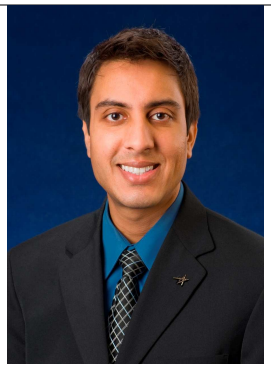
Jatin Mehta



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Biography

Jatin Mehta, Senior Electrical Engineer, started his career at Lockheed Martin Missiles and Fire Control (MFC) in 2007 as a member of the Electrical Engineering (EE) department. His first role at Lockheed Martin was working on the Patriot Advanced Capability (PAC-3) Guidance Processor Unit Redesign (GPUR) effort where he developed a printed circuit board. When this project was completed, he had made quite an impression on not only his immediate team but also with the leadership of the entire EE department. He is now requested by name as a subject matter expert to work any digital/FPGA design project. After GPUR, Jatin transferred to the Power Management team to work a digital and analog design on a high current AC Link Controller project.

FPGA design encompasses using a hardware description language such as VHDL or Verilog to complete application specific functions by customizing the FPGA integration circuit. This includes the logic development of hardware instantiations, functional simulations, and timing analysis. Processor design is the detailed design of a circuit board similar to a computer's motherboard with a central processor, memory, multiple input/output interfaces, and other application specific features. Both of these areas are critical as the future of electronics combines more functionality into single integrated circuits and data buses continue to increase in frequency. Jatin excels in both of these areas and is forging new designs to incorporate emerging technologies into future LMMFC products. Jatin has pioneered system engineering tools to effectively manage and disseminate complex requirements, guidance, and processes/procedures for the new Nuclear Systems and Solutions (NSS) business area. One of his most significant accomplishments was when he was recruited to work a Corporate Focus Item Information Assurance Internal Research & Development.

Jatin was recently awarded with a Patent Award for the AC Link Converter Switch Engine and a Lockheed Martin Trade Secret Award. Jatin is currently serving as the lead processor board designer for the PAC-3 Launcher Modification Kit processor redesign effort. Jatin is quite innovative and placed in the 2012 Size, Weight, and Power (SWaP) Contest. Jatin recently won the 2013 Electrical Engineering Innovation competition. His idea has the potential to spawn a technology IRAD, providing a new growth area for the company.

Jatin has led the Asian Heritage Association (AHA) Employee Resource Group (ERG) for the past 3 years and is currently serving as the AHA chairman. In addition, he frequently helps community outreach programs and volunteers in the Kennedy Middle School Mentoring Program. Jatin received a Bachelor's of Science in Electrical Engineering and a Master's of Science in Electrical and Computer Engineering from the University of Texas.